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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/769,418	01/26/2001	Hisaji Murata	60188-027	4237

7590 08/11/2005

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EXAMINER

DESIR, JEAN WICEL

ART UNIT	PAPER NUMBER
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2614

DATE MAILED: 08/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/769,418

Applicant(s)

MURATA ET AL.

Examiner

Jean W. Désir

Art Unit

2614

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 26 January 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Secher et al (US 5,367,331) in view of Yasuda et al (US 5,889,549).

Claim 1:

Secher discloses:

“an A/D converter for sampling an analog video signal at a first frequency and converting the analog video signal into a digital video signal”, see Fig. 4 item 014;

“a Y/C separator for separating the digital video signal into a first luminance signal and a first chrominance signal; a chroma decoder for demodulating the first chrominance signal into a first set of color-difference signals”, see Fig. 4 item 015, col. 3 lines 28-61;

“a first D/D converter for re-sampling the first luminance signal and the first set of color-difference signals at a second frequency”, see Fig. 4 items 022, 041 see col. 3 lines 28-61;

“a digital codec for digitally encoding the first luminance signal and the first set of color-difference signals, which have been output from the first D/D converter, to produce

a write signal, and for sampling a digitally encoded read signal at the second frequency to decode the read signal into a second luminance signal and a second set of color-difference signals; a chroma encoder for modulating the second set of color-difference signals into a second chrominance signal", see Fig. 4 items 022, 041, 3, 052;

"and D/A converters for sampling the luminance and chrominance signals that have been selected by the data selecting means at the frequency of the clock signal that has been selected by the clock selecting means, and for converting the luminance and chrominance signals into analog signals", see Fig. 4 items 052, 053;

the claimed limitation "data selecting means for selecting either the first luminance and first chrominance signals, which have been output from the separator, or the second luminance and second chrominance signals, which have been output from the codec and the encoder, respectively; clock selecting means for selecting a first clock signal with the first frequency if the data selecting means has selected the first luminance and first chrominance signals or a second clock signal with the second frequency if the data selecting means has selected the second luminance and second chrominance signals" is not explicitly disclosed by Secher, as claimed. However, Yasuda teaches a video decode-signal changeover switch unit (see Fig. 1) and thus the principle of switching between a locally captured video signal and a remotely captured signal for display (data selecting means); and clocks under the direction of system controller 14 (see Fig. 1) are selected/routed (clock selecting means). An artisan would be motivated to combine the references to arrive at the claimed invention, this combination would provide optimum coding of transmitted data and optimum

reproduction of received data. Therefore, the claimed invention would have been obvious to a person of ordinary skill in the art at the time the invention was made.

Claim 2 is disclosed, see Secher at col. 3 lines 42-61, col. 4 lines 49-57, col. 9 lines 51-60.

Claim 3 is disclosed, see Secher at col. 3 lines 42-61, col. 4 lines 49-57.

Claim 4 is disclosed, see Secher at col. 6 line 56 to col. 7 line 15, col. 4 lines 49-57.

Claim 5 is rejected for the same reasons as claim 1, a second D/D converter as claimed is met by Secher Fig. 4 items 041, 052.

Claim 6 is rejected for the same reasons as claim 5.

Claim 7 is rejected for the same reasons as claim 1.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean W. Désir whose telephone number is (571) 272 7344. The examiner can normally be reached on 5/4/9 - First Friday Off.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John W. Miller can be reached on (571) 272 7353. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JWD

Aug. 8, 05



JOHN MILLER
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600